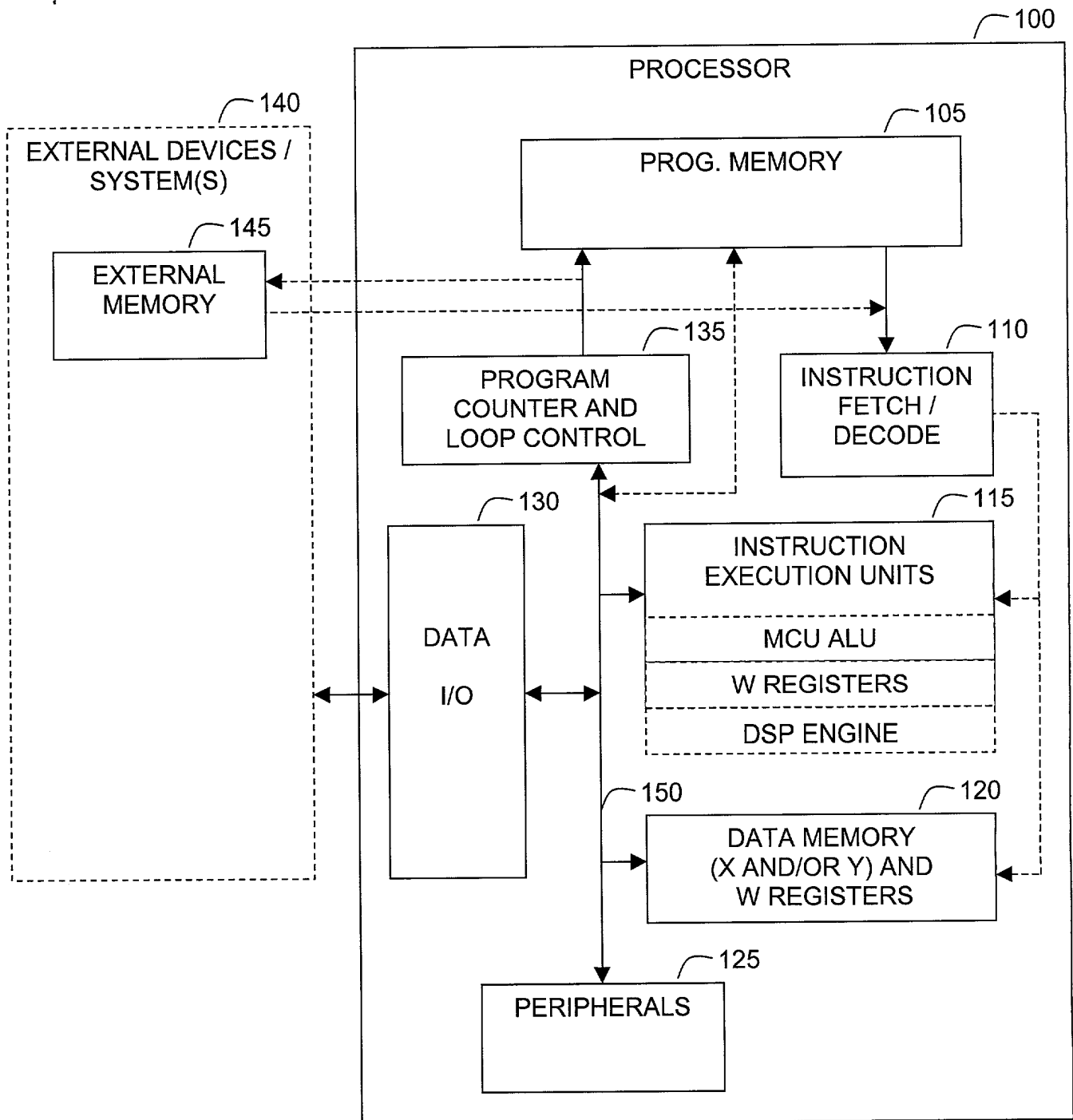
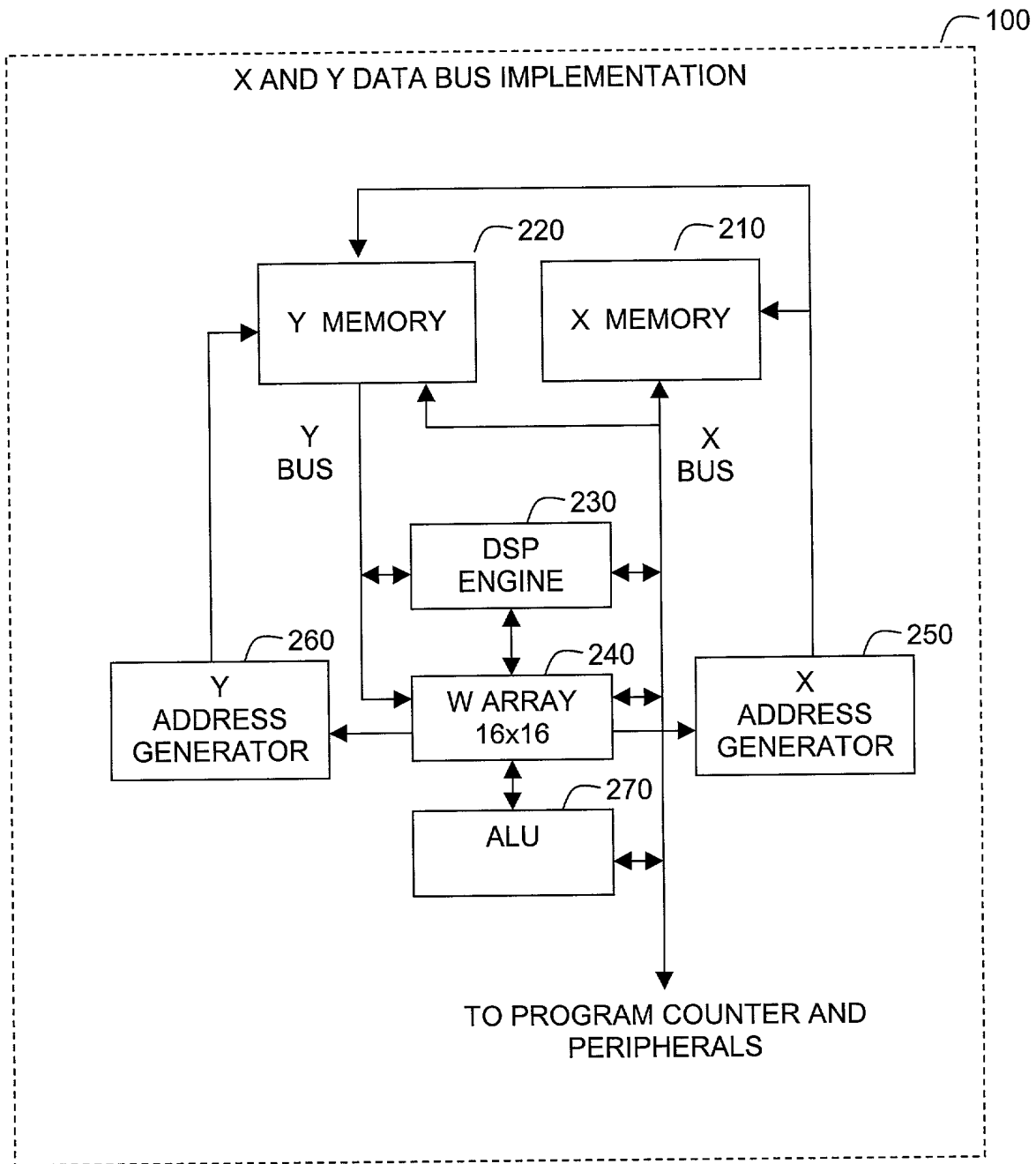


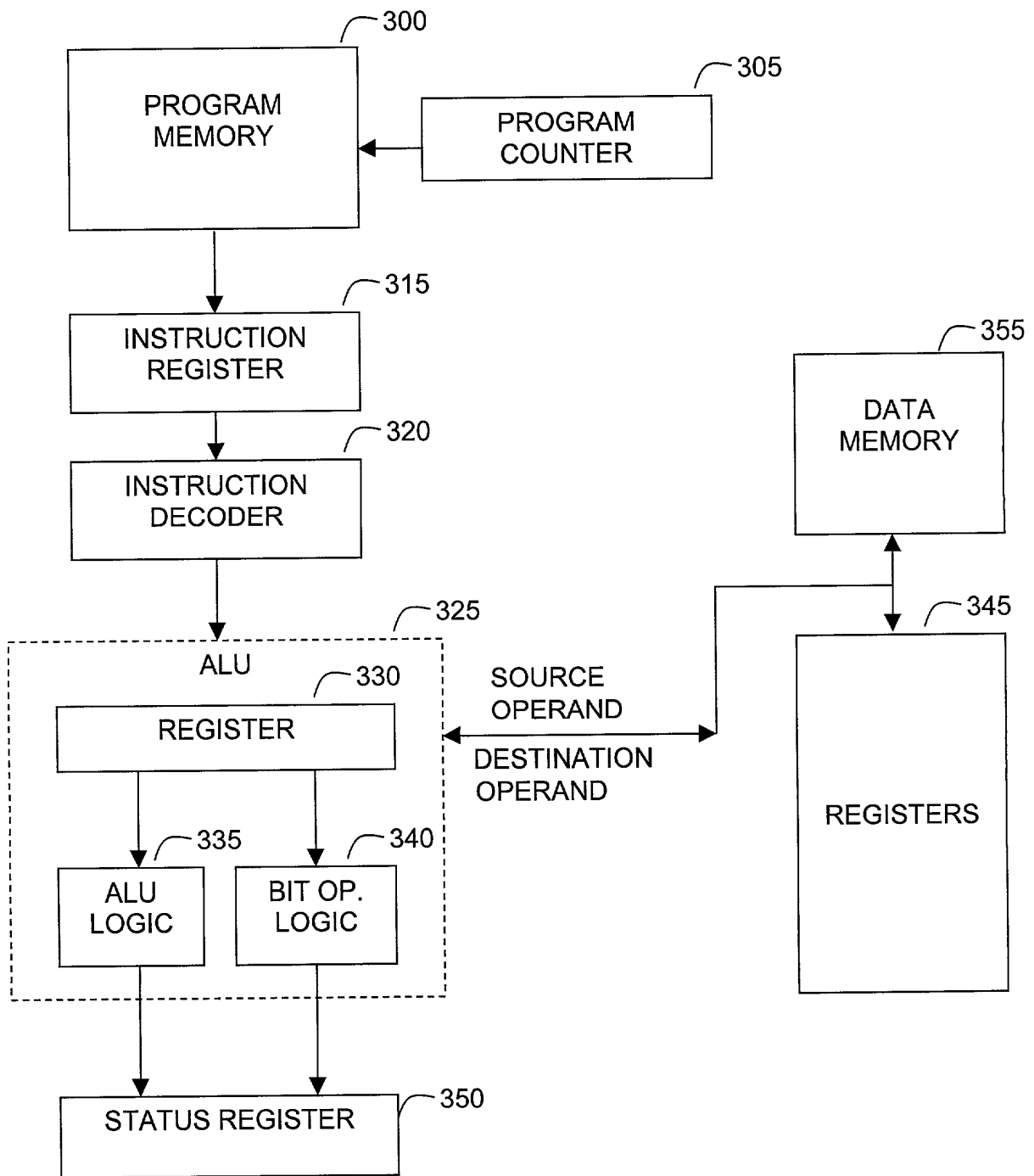
FIG. 1



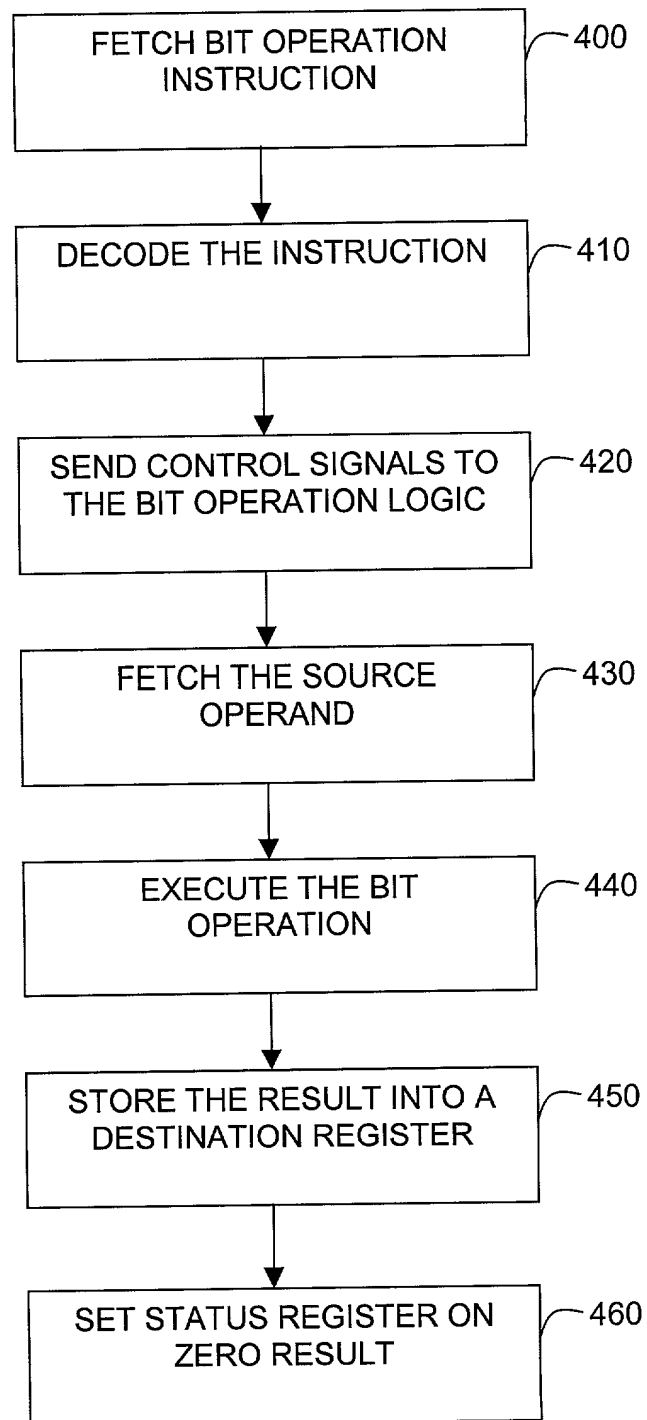
**FIG. 1**



**FIG. 2**



**FIG. 3**



**FIG. 4**

## Bit Operation Instructions

FBCL {b} Ws Wd  
FBCR {b} Ws Wd

Find First Bit Change From Left  
Find First Bit Change From Right

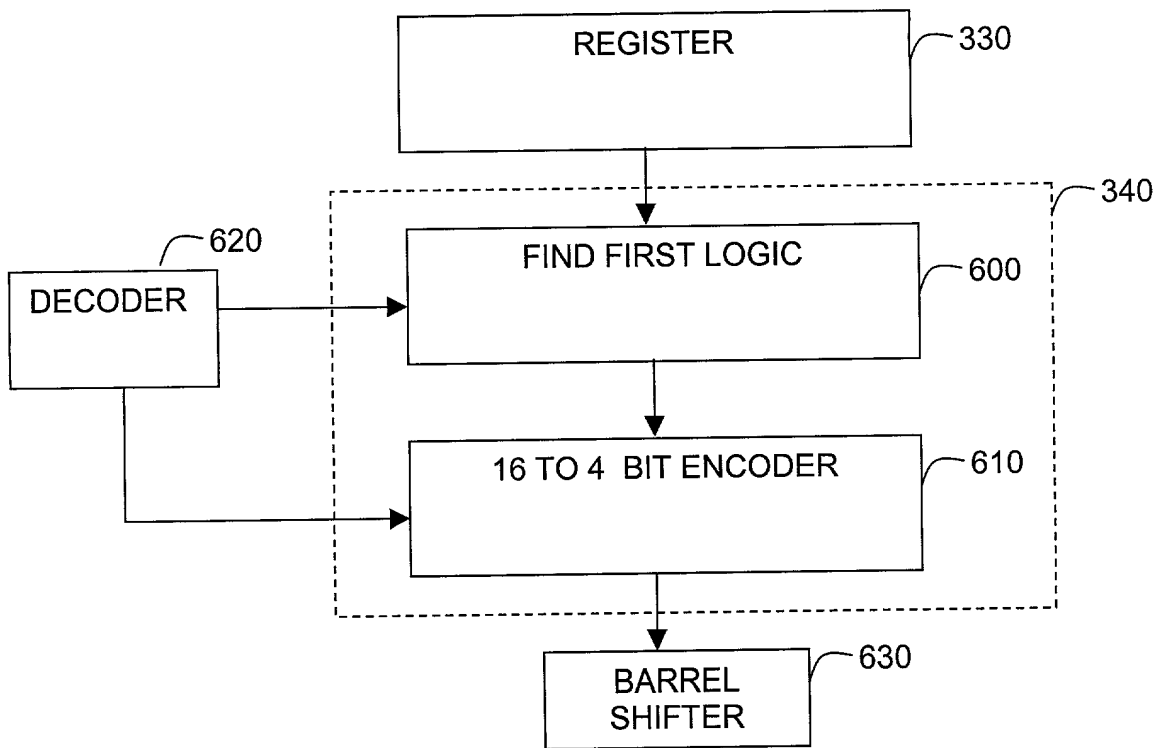
FF0L {b} Ws Wd  
FF0R {b} Ws Wd

Find First Zero From Left  
Find First Zero From Right

FF1L {b} Ws Wd  
FF1R {b} Ws Wd

Find First One From Left  
Find First One From Right

**FIG. 5**



**FIG. 6**